

**METHOD AND APPARATUS FOR USING SDRAM TO READ AND WRITE DATA  
WITHOUT LATENCY**

**ABSTRACT**

An apparatus and method are provided for transmitting  
5 data using synchronous dynamic random access memory (SDRAM).  
In example, the method includes writes data using a first set  
of SDRAM banks. Data is written using a second set of SDRAM  
banks, wherein the first set of SDRAM banks and the second set  
of SDRAM banks write interleaved. Data is read using a third  
10 set of SDRAM banks. Data is read using a fourth set of SDRAM  
banks, wherein the fourth set of SDRAM banks and the third set  
of SDRAM banks read interleaved.